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sues Rej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agree
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This collection of information is required by 37 CFR 1.133. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 21 minutes to complete, useful on the properties and authorities the completed application form to the USPTO. Time will vary depending upon the individual case. Any common to the unional of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1430, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

one to or

# Proposed Amendment

(Original) A radially distributed serial control bus architecture,
 comprising:

a controller:

a plurality of connections, each connection having a first end and a second end,
each of said first end of said each connection coupled to said controller; and
a plurality of peripheral cards each peripheral card individually coupled to a
second end of a respective dedicated one of said plurality of connections

31. (Amended) A method of fault isolation in a serial control bus architecture, comprising the steps of:

detecting a signal communication failure on a data control bus coupled to a plurality of peripheral devices;

performing a control bus integrity check responsive to the signal communication failure using the control bus architecture independent of the peripheral devices; and

isolating the origin of said signal communication failure on said bus to a particular peripheral device.

34. (Amended) A radially distributed serial control bus <u>architecture</u> to isolate data faults sent to a data bus, comprising:

a data bus;

- a plurality of peripheral cards to transmit data on the data bus; and
- a controller, coupled to the data bus and separately coupled to each of the plurality of peripheral cards, the controller performing an integrity check to isolate a fault to one of the plurality of peripheral cards independent of the plurality of peripheral devices.



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## FACSIMILE TRANSMISSION

### CONFIDENTIAL

**DATE:** August 27, 2004

**CLIENT-MATTER No.: 20852-09526** 

To:			
NAME	FAX NO.	PHONE NO.	
Examiner Clifford H. Knoll, Unit 2112 U.S. Patent and Trademark Office	(703) 746-3184	(703) 305-8656	
RE: App. No.: 09/828,481			

FROM:

Dorian Cartwright

PHONE:

(650) 335-7247

ORIGINAL WILL NOT FOLLOW NUMBER OF PAGES WITH COVER PAGE: 3

#### MESSAGE:

Per you instructions, and in order to advance prosecution, please find enclosed proposed amendments with an interview request form. I appreciate this opportunity and look forward to speaking with you shortly. You can call (650)335-7247 and leave the scheduled time on voice mail if I am not available.

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